

a second switching transistor;

an output pair of serially coupled complementary type transistors, a first one of the pair of transistors having a first electrode coupled to a source of the third voltage level through the first switching transistor and a control electrode coupled to the second electrode of the input transistor, a junction between the output pair of transistors providing the output terminal for the level-shifting circuitry, a control electrode of the second one of the pair of transistors being connected to the second electrode of the input transistor, the second one of the pair of transistors having a second electrode coupled to the fourth voltage level through the second switching transistor; and

wherein the first and second switching transistors are fed by the enable/disable signal.

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7. (Twice Amended) The level shifting circuitry recited in claim 2 and further comprising an additional transistor having a control electrode coupled to the junction, a first electrode coupled to the source of the third voltage level through the first switching transistor and a second electrode coupled to the second electrode of the input transistor.

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23. (Amended) The circuit of claim 22 wherein the enable/disable section comprises:

a third transistor with a current path coupled in series the current path of the first transistor, the current path of the third transistor coupled between the third reference voltage node and the first transistor; and

a fourth transistor with a current path coupled in series the current path of the second transistor, the current path of the fourth transistor coupled between the first reference voltage node and the second transistor.

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